

CMOS Imager Design Engineer

Draper Laboratory in Cambridge, Ma is looking for a CMOS Imager Design Engineer. Draper Laboratory is an independent, not-for-profit engineering research and development organization. Its mission is to serve the national interest in applied research, engineering development, education and technology transfer.

This opportunity is for an experienced CMOS device engineer to work on cutting edge imager technology development. Individual will assume lead responsibility for CMOS imager and camera design, and will work closely with the development team on detailed sensor characterization and data analysis. Significant experience with mixed-signal VLSI CMOS transistor level design, simulation, and physical layout is required. Responsibilities include design and analysis of ultra-low noise analog circuit blocks, low-power analog-to-digital conversion, high-speed RF, analog and digital signal transmission, and digital signal processing. Individual is to provide oversight of design release to sensor manufacture (external fabrication). Successful candidate is also expected to contribute to concept generation in support of custom imaging applications.

Qualifications

M.S. or Ph.D. in electrical engineering or physics with a concentration in semiconductor device physics and a minimum of 10 years of directly related experience desired. Demonstrated track record in bringing significant CMOS imager design enhancement to successful fruition is required. Knowledge of CCD & CMOS device operation, fabrication process flow, integration, and device characterization required; working knowledge of optics and material analysis techniques is a plus.

Applicants must be US citizens.

To apply send a resume to nbeaumont@draper.com