



Walter F. Kosonocky (M'65–SM'70–F'76) received the B.S. and M.S. degrees in electrical engineering in 1955 and 1957, respectively, from the Newark College of Engineering, Newark, NJ, and the Sc.D. degree in engineering in 1965 from Columbia University, New York.

From April 1987 to November 1996, he was a Distinguished Professor in the Electrical Engineering Department and holder of the Chair in Optoelectronics and Solid-State Circuits at the New Jersey Institute of Technology, Newark. From 1955 through March 1987, he was employed at the David Sarnoff Research Center (formerly RCA Laboratories), Princeton, NJ. He became a Member of the Technical Staff in 1956, and was appointed Fellow of the Technical Staff in 1979. From April 1987 to November 1996, he was associated with the David Sarnoff Research Center as a Consultant on Solid-State Image Sensors. At the Center, he was engaged in research on solid-state devices, circuits, and systems applications. This work included: from 1955 to 1961, development of ferrite memories and investigations on parametric and tunnel diode digital circuits; from 1961 to 1970, applications of lasers as switching devices, optical

hologram memories, and MOS sensor arrays; from 1970 to 1977, the development of charge-coupled devices for memories, signal processing, and image sensors; and from 1977 to 1996, the development of processing and designs for infrared CCD imagers with platinum silicide Schottky-barrier photosensors. He was an author or coauthor of 77 technical papers and was issued 47 U.S. patents.

Dr. Kosonocky shared four RCA Laboratories Outstanding Achievement Awards (in 1959, 1963, 1980, and 1984) and two David Sarnoff Awards for Outstanding Technical Achievement (in 1981 and 1984). In 1985, he received the IEEE J. J. Ebers Award for “pioneering and innovative contributions to the development of charge-coupled devices and Schottky-barrier infrared image sensors.” He was a member of Tau Beta Pi, Eta Kappa Nu, Sigma Xi, SPIE, and SPSE. From 1973 to 1977, he was Chairman of the Solid-State Circuits Committee of the IEEE Circuits and Systems Society. From 1974 to 1978, he was Chairman of the Integrated Circuits Technology Committee of the IEEE Electron Devices Society. He served on the Technical Program Committees of the 1977 International Electron Devices Meeting, and, from 1976 to 1981, the International Solid-State Circuits Conference, where he served as Technical Program Chairman in 1979. From 1979 to 1982, he was Chairman of the IEEE TRANSACTIONS ON ELECTRON DEVICES. He was also the Guest Editor of Joint Special Issues of the TRANSACTIONS ON ELECTRON DEVICES on VLSI Technology for April 1979, August 1980, and April 1982, and on Solid-State Image Sensors for August 1985. He was Chairman of the 1981 Symposium on VLSI Technology, and, starting in 1985, was Chairman of the IEEE Executive Committee for this Symposium. He was the Technical Program Chairman of the 1984 Electronic Imaging Conference (EIC) and the Chairman of the 1985 EIC.